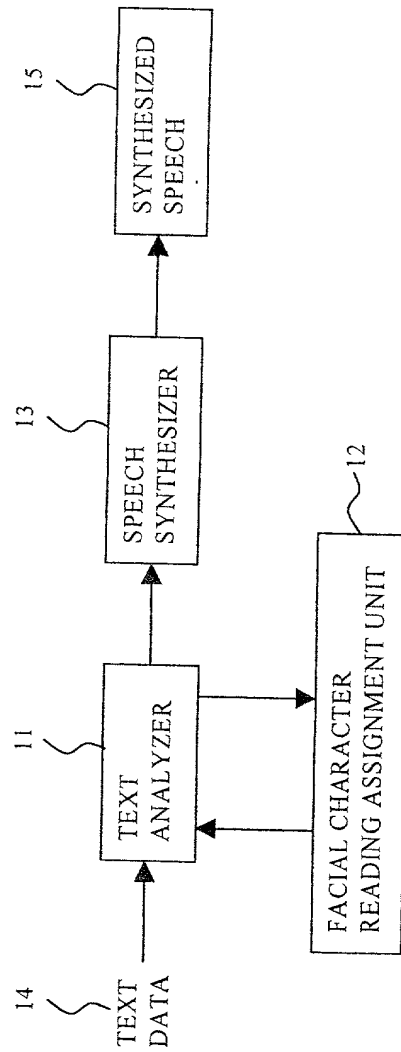
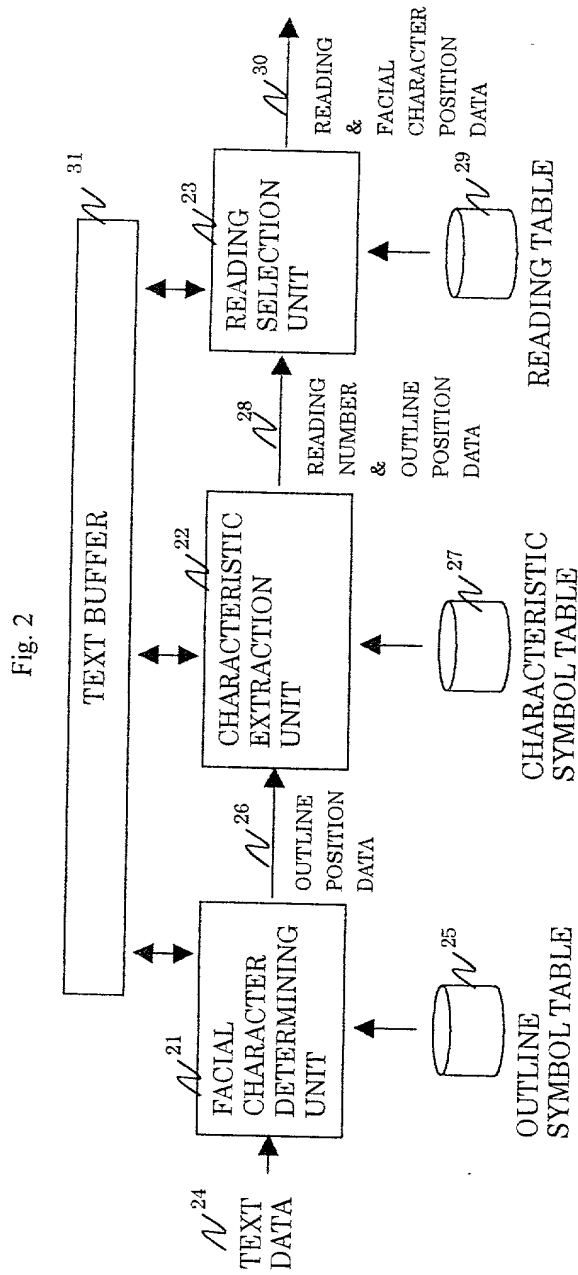


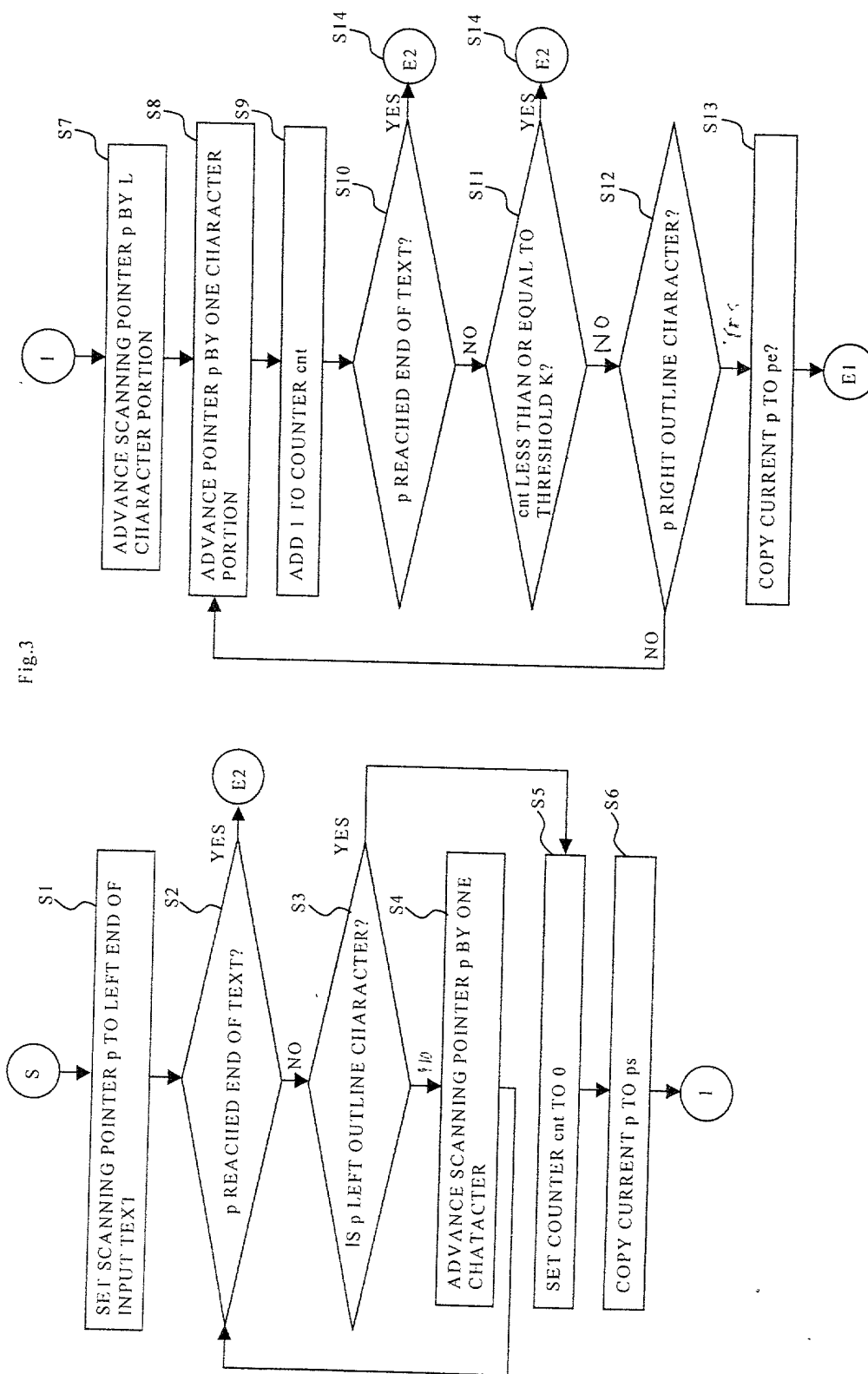
Fig. 1



OVERALL CONFIGURATION OF TEXT TO SPEECH SYNTHESIZER

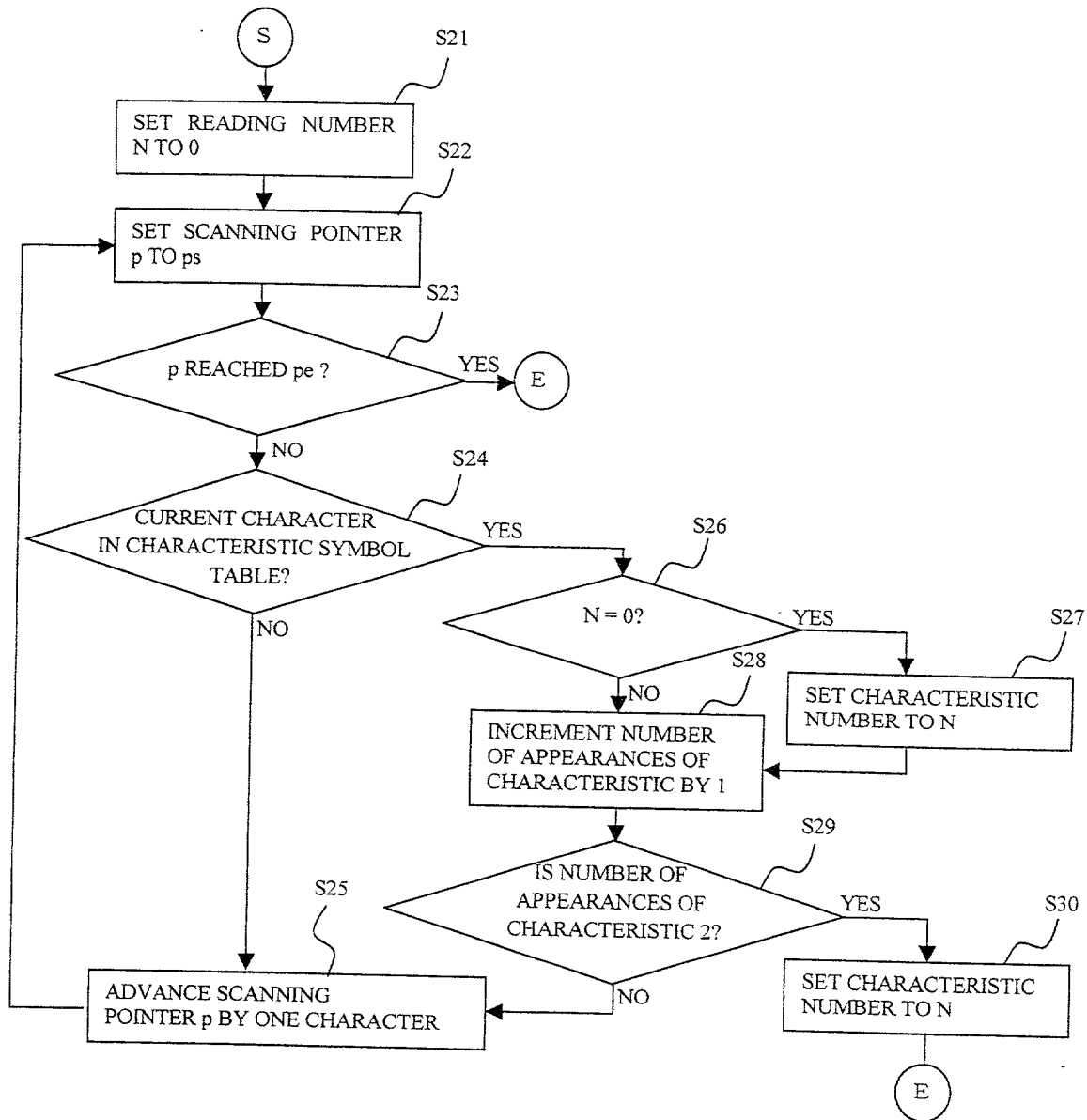


CONFIGURATION OF THE FACIAL CHARACTER READING ASSIGNMENT UNIT OF THE FIRST EMBODIMENT



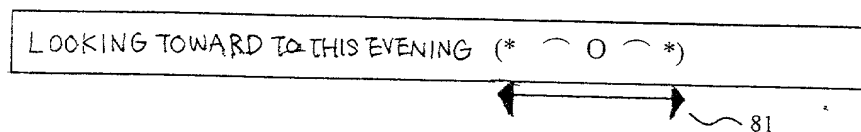
FLOW OF PROCESSING FOR FACIAL CHARACTER DETERMINING UNIT

Fig. 4



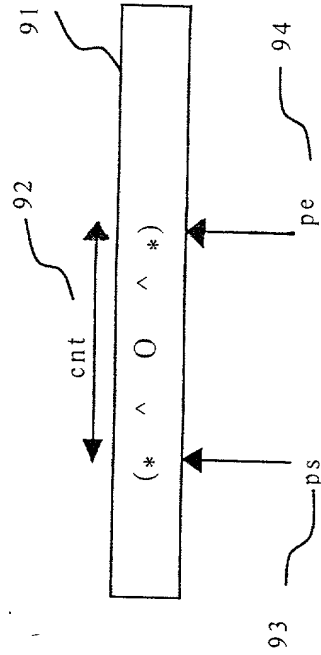
PROCESSING FLOW FOR CHARACTERISTIC EXTRACTION UNIT

Fig. 5

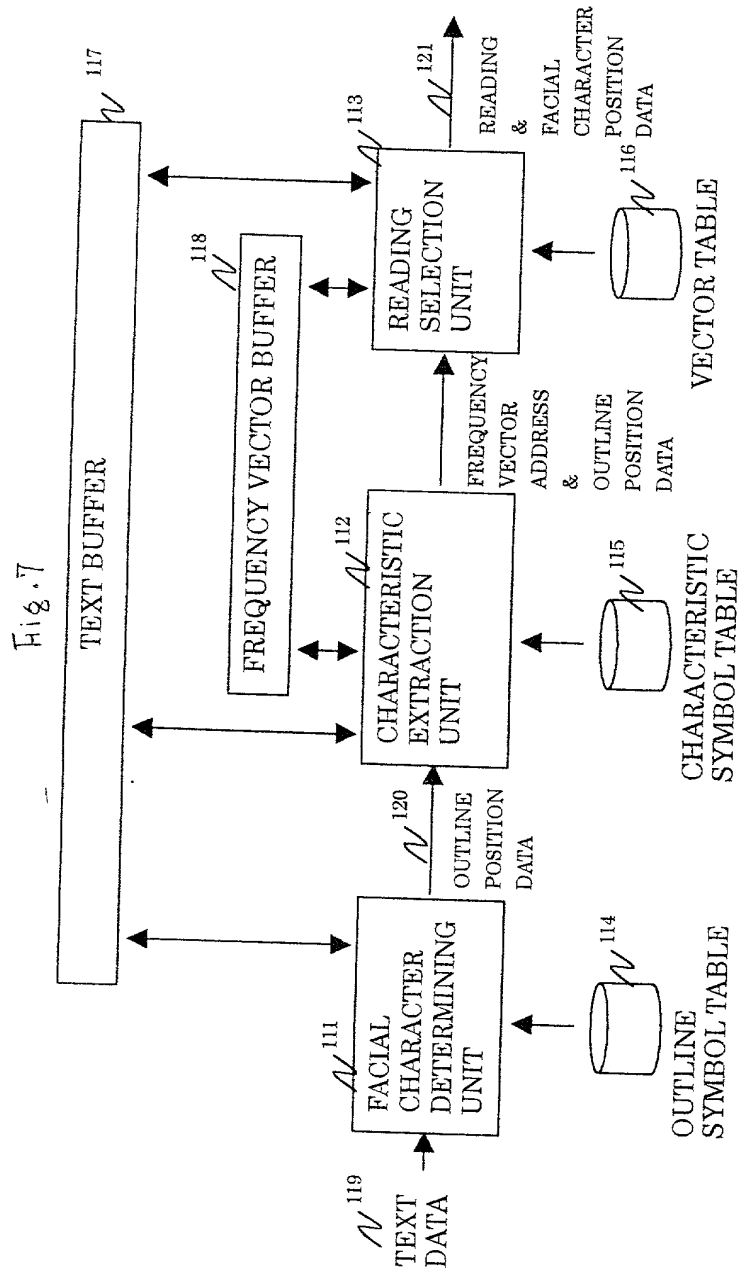


EXAMPLE OF TEXT DATA PASSED OVER TO READING ASSIGNMENT UNIT

Fig. 6

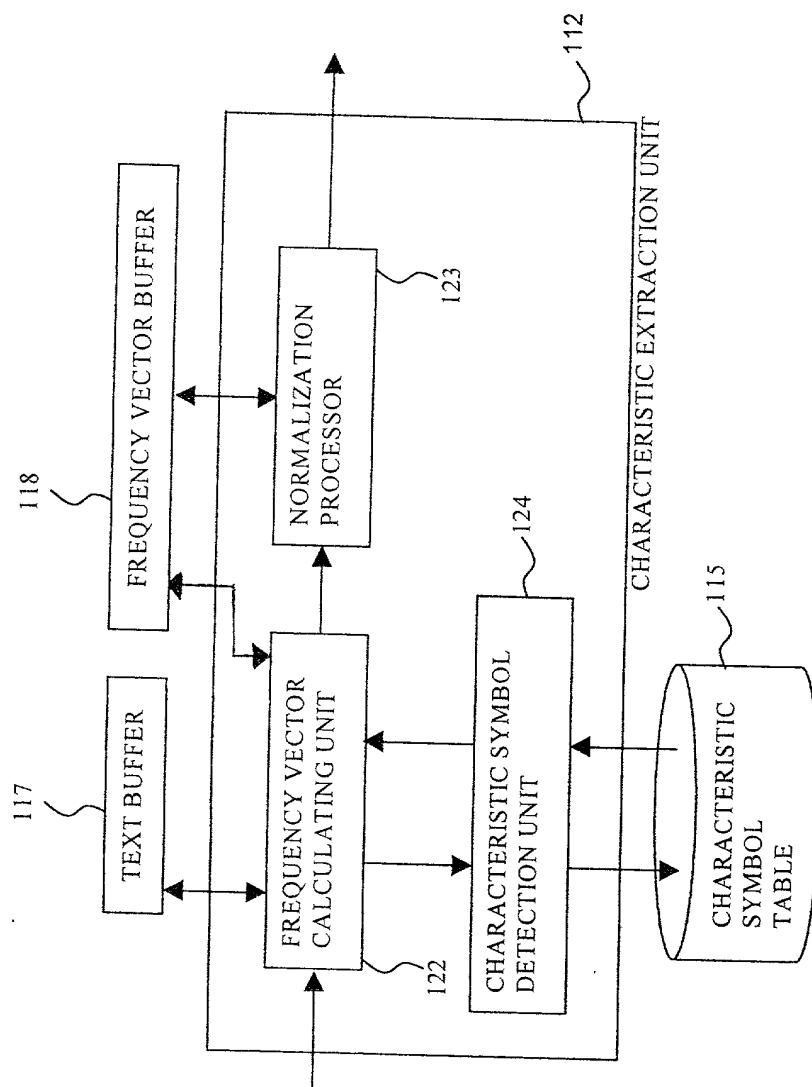


EXAMPLE OUTPUT OF FACIAL CHARACTER DETERMINING UNIT

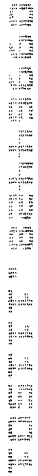


VIEW OF CONFIGURATION OF FACIAL CHARACTER READING ASSIGNMENT UNIT OF THE SECOND EMBODIMENT

Fig. 8



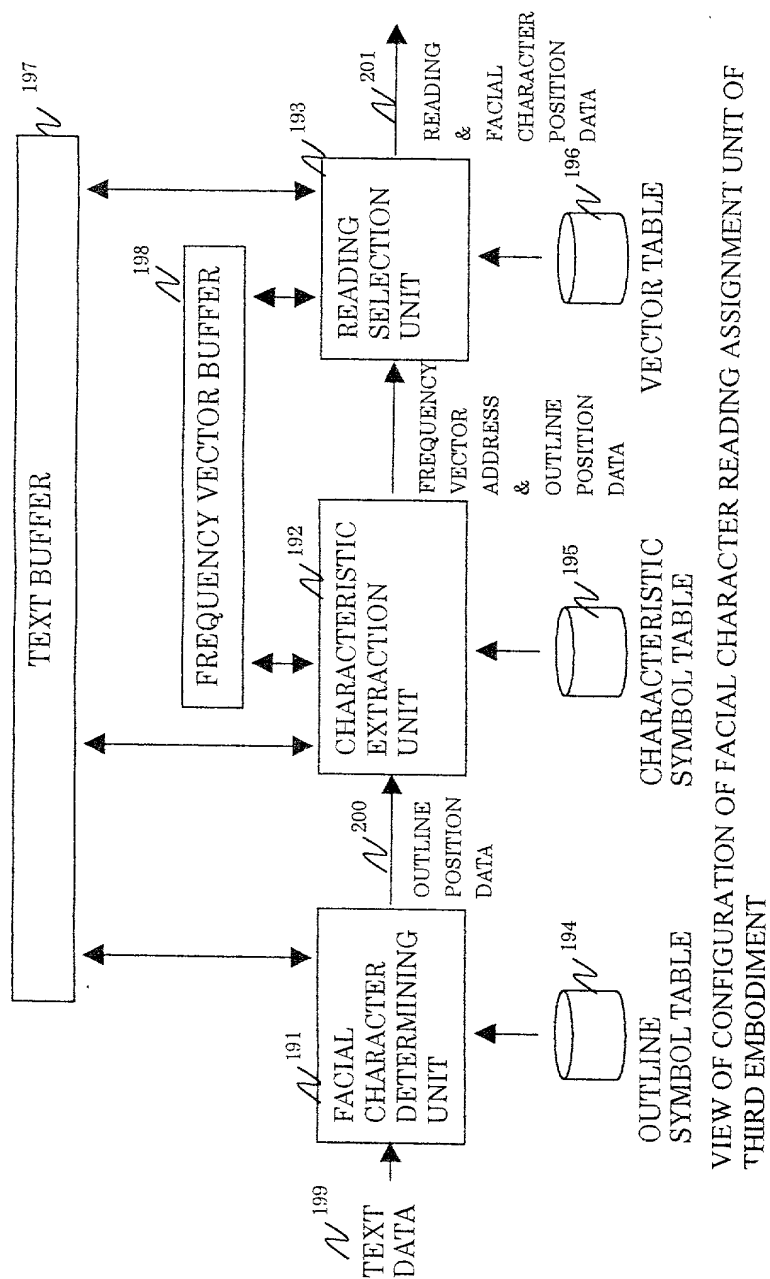
VIEW OF CONFIGURATION OF CHARACTERISTIC EXTRACTION UNIT

[illegible][illegible]

[illegible]

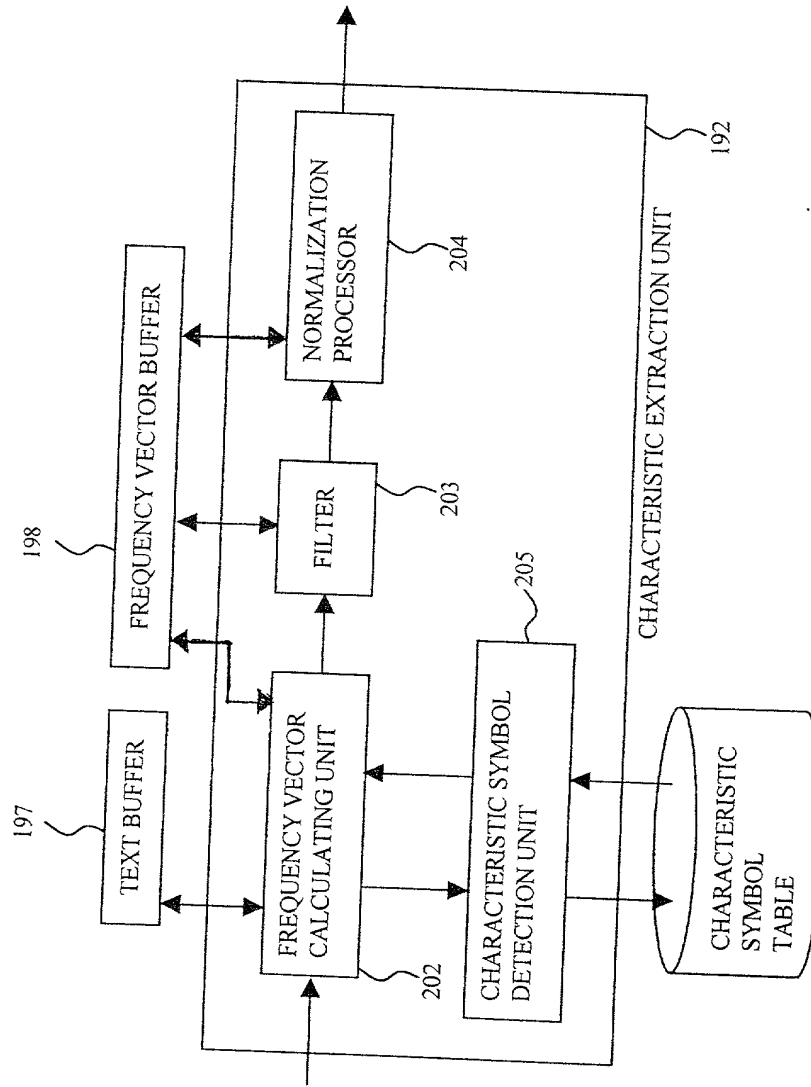
EXAMPLE OF SELECTED TYPICAL VECTORS

Fig. 13



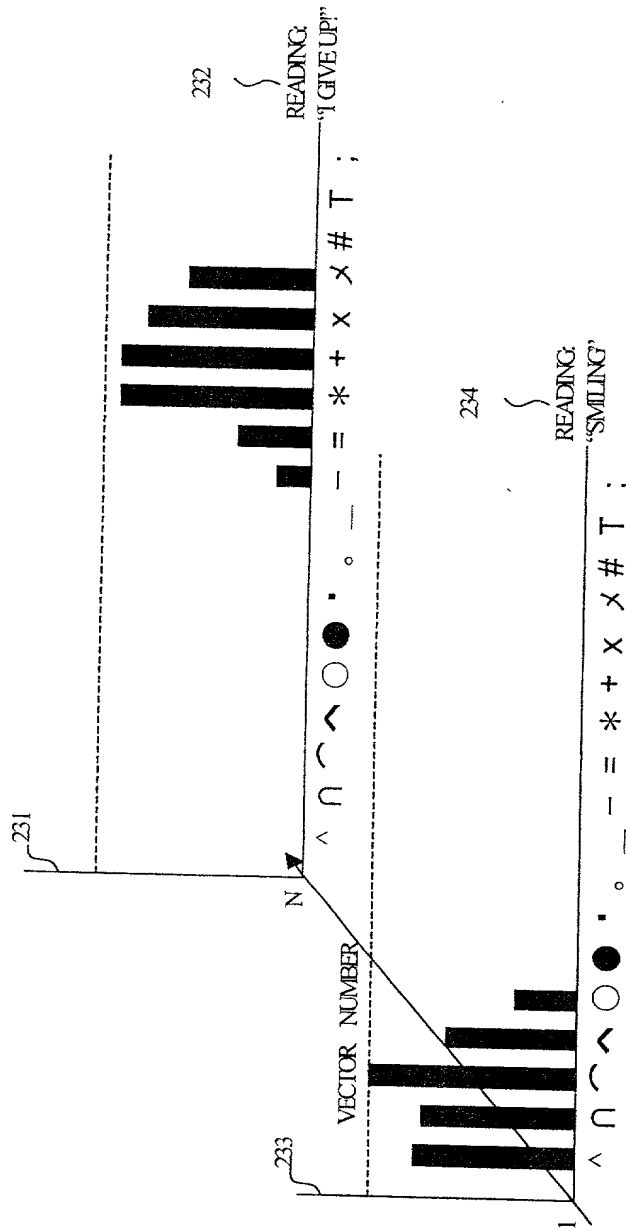
VIEW OF CONFIGURATION OF FACIAL CHARACTER READING ASSIGNMENT UNIT OF THIRD EMBODIMENT

Fig. 14



VIEW OF CONFIGURATION OF CHARACTERISTIC EXTRACTION UNIT

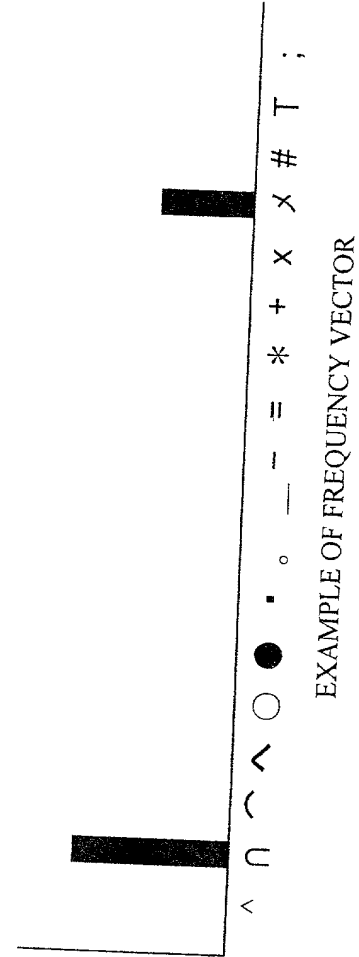
Fig. 15



EXAMPLE VECTOR TABLE

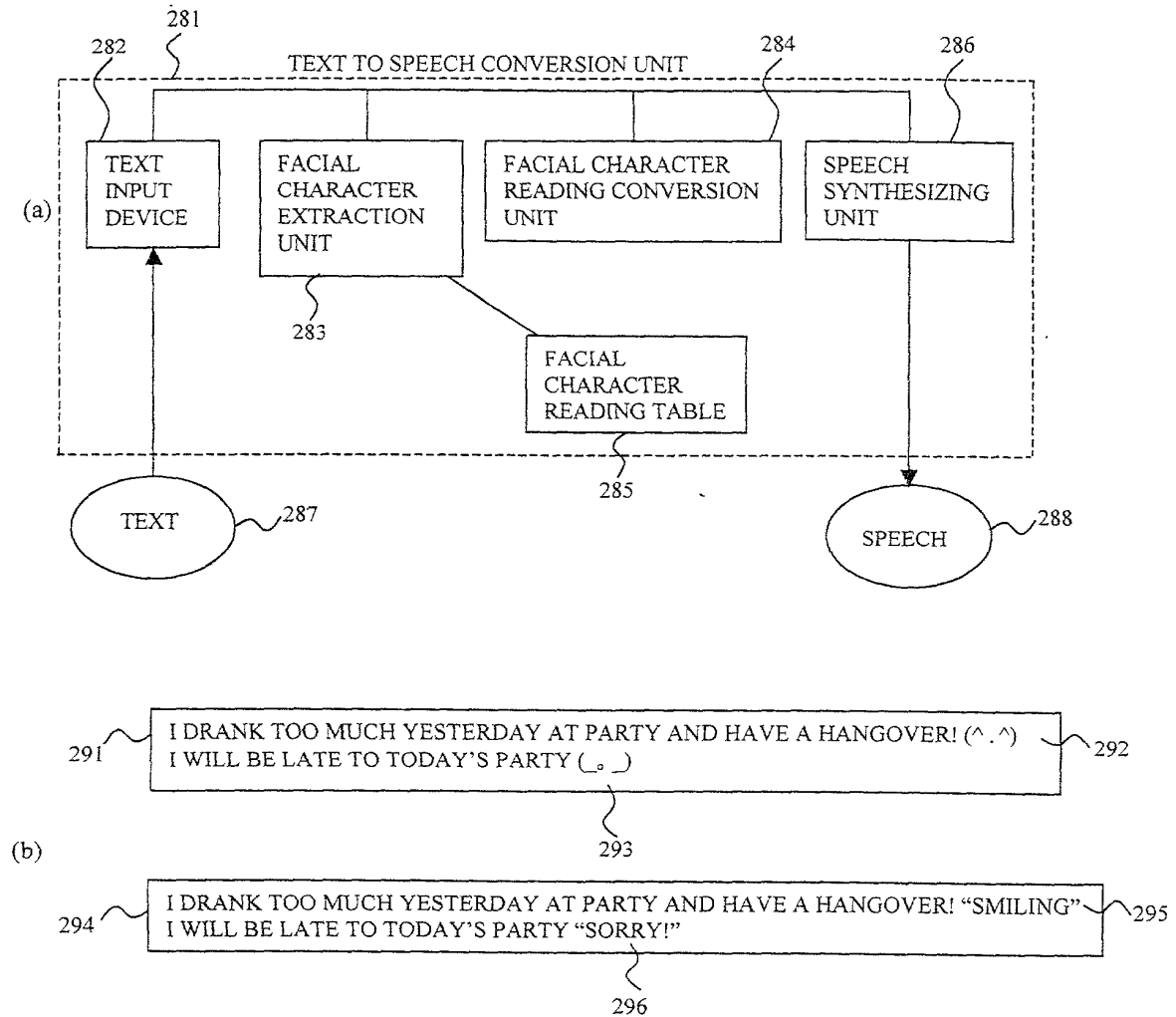
Diagram illustrating a memory array structure. A word line 242 contains the bit pattern $(x \quad n \quad n)$. This word line is connected to a bit line 243 via a sense amplifier 244. The sense amplifier 244 is connected to a counter 245. The counter 245 is connected to a control line 246.

Fig. 17



[illegible]

Fig. 20



VIEW DESCRIBING RELATED ART